



Coimbatore Institute of Technology
(Government Aided Autonomous Institution)
Department of Computer Science and Engineering
Avinashi Road, Coimbatore-641014 India

Advt. No. CIT/IT/ICPS/ GK/01/2020

Dated – 03/12/2020

Advertisement for the Post of Senior Research Fellow (SRF) and Junior Research Fellow (JRF) (On Contract) under the Cyber Security Research of Interdisciplinary Cyber Physical System (ICPS) Project

Applications are invited for the post of Senior Research Fellow (SRF) and Junior Research Fellow (JRF) (on contract) under the Cyber Security (CS) Research of Interdisciplinary Cyber Physical Systems (ICPS) programme project entitled **“Complete Packet Analysis Based Cyber-Attack Detection System for Industrial Internet of Things”** in reference to the sanction letter DST/ICPS/Cluster/CS Research/2018 dated: 13.03.2019 by Department of Science & Technology, Ministry of Science and Technology, New Delhi-110016. The duly completed application on prescribed format along with copies of supporting documents must reach to **Dr. G.Kousalya, Principle Investigator, Department of Computer Science and Engineering, Coimbatore Institute of Technology, Avinashi Road, Coimbatore -641014 ,on or before 20/12/2020.** The envelope should be super scribed as **“Application for the post of Senior Research Fellow (SFR) and Junior Research Fellow (JRF) (on contract) in the ICPS project on “Complete Packet Analysis Based Cyber-Attack Detection System for Industrial Internet of Things”**. A soft copy of the application should also be sent to kousalya.g@cit.edu.in / citdsticps@gmail.com (Principal Investigator of the project) with the same subject.

Title of the Project: “Complete Packet Analysis Based Cyber-Attack Detection System for Industrial Internet of Things”

Name of the Position: On Contract- Senior Research Fellow (SFR) and Junior Research Fellow (JRF)

Number of Position: Two -SRF- 1 position and JRF -1 position

Project Duration : Three (03) Years

Age Limit: Maximum 28 Years as on 30/11/2020

Essential Qualifications: M.E/M.Tech in Computer Science and Engineering/ Information Technology/ Electronics / Electrical Engineering.

A GATE/NET or similar other national level ranking with Strong Programming skills in C and Python- Compiler Design , Networking (Except Physical Layer) , Security issues related to these layers and tools used.

Fellowship: SRF 28,000/- pm (Fixed) + 10% HRA , JRF 25,000/pm (Fixed) +10% HRA

Note(s)

1. The selected candidate should visit different research centres in India as and when required.
2. The applicant will be responsible for the authenticity of information, other documents and photographs submitted.
3. The Institute reserves the right to accept application at any time, and consider candidates of exceptional credentials without applications. Qualification and experience may be relaxed at any point of time for exceptional candidates.
4. Mere, possessing the prescribed qualification does not ensure that the candidate would be called for Interview. The Candidates will be shortlisted on the basis of merit and need of the project.
5. Shortlisted Candidates will be informed by e-mail about the interview date. So, the candidate must provide valid E-mail IDs in their applications.
6. Shortlisted candidates have to present themselves for the interview on the interview date with updated CV and original and attested photocopies of mark sheets/certificates in support of their academic qualifications.
7. Applicants in employment (private, government or any other organization) are required to submit a “**No Objection Certificate**” from the employer at the time of interview.
8. No TA/DA will be paid for appearing in the interview.
9. The first and foremost responsibility of the candidate is to fulfil the jobs’ requirements to complete the above mentioned project in timely manner.
10. The association between PI and the candidate can be discontinued at any time by giving one month’s notice in writing either by the PI of the project to candidate, or by candidate to the PI of the project.
11. Candidate selected for this position can also apply for the regular PhD program in CSE Department, provided they satisfy the eligibility criterion followed by selection process laid down by the institute and within the project duration the selected candidates should complete their PhD program.
12. After qualifying the PhD admission / selection test, the candidate may register as regular PhD scholar and eligible to draw the fellowship and other allowances as per the Institute rules.
13. This appointment will be purely contractual. Initially appointment will be for first one year. It will be renewed for another year or till the end of project whichever is earlier based on the performance of the candidate and recommendation of the selection/review committee.
14. The candidates who have already applied for the PhD admission in the department of Computer Science and Engineering for July 2018 and July 2019 batch and willing to join as SRF and JRF are required to complete the application form (annexure-I) and send to kousalya.g@cit.edu.in / citdsticps@gmail.com
15. Hostel accommodation may be provided as per Institute rules.

16.CANDIDATE who Have already applied need not submit the documents again

**17.LAST DATE FOR ONLINE APPLICATION SUBMISSION 20/12/2020 -
Online Test on 21/12/2020 at 10.00AM . Interview for selected candidates will
be informed through e-mail.**

18.Neatly typed application form as per the given application format.

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Dated.....

APPLICATION FORM

1. Name of the Candidate (In Capital):
2. Sex (Male/Female):
3. Marital Status (Married/Unmarried):
4. Date of Birth:
5. Age (as on last date of application): _____ Years _____ Months _____ Days
6. Father's/Husband's Name:
7. Present/Contact Address:

8. Permanent Address:

9. Mobile No:
10. E-mail:
11. Nationality:
12. Category Gen/SC/ST/OBC/PH:

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Dated – 17/11/2020

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Dated.....

APPLICATION FORM

13. GATE/NET

Branch	Year	Valid up to	Percentile (& Score)	All India Rank

14. Educational Qualification (10th onward):

Qualification	Subject/Discipline	Board/Institute/Univ.	Year of passing	% of marks	Division
10th					
12th					
Diploma, if any					
B.E./B.Tech					
M.E./M.Tech					

15. Summary of M.E./M.Tech Thesis (Maximum 150 words): **Enclose a separate sheet**

16. Research Publications (Maximum Best 5): **Enclose a separate sheet**

17. Details of Experience in chronological order. **Enclose a separate sheet**

18. Any other information: **Enclose a separate sheet**

19. Indicate whether you propose to register for higher degree Anna University, Chennai:

Yes/No

(Name of higher degree is Ph.D.)

DECLARATION

I hereby declare that all the statements made in this application are true and complete and nothing has been concealed/ distorted. I am aware that, if at any time I am found to have concealed/distorted any material information, my engagement is liable to be summarily terminated without notice.

Place:

Signature of the Applicant

Date:

Checklist-

Scanned Self Attested copy of

1. Resume as per the above format
2. Proof of Date of Birth
3. Mark sheets & certificates of all the examination passed
4. Category Certificate, if applicable
5. GATE/NET score card
6. Experience certificate, if any