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Advanced Technical support- Ericsson

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Managing director,
KSNM Marketing, Coimbatore.

Dr. S.Karthik,
Managing director,
Analog Devices, Bangalore.

Dr. K.Gridhar,
Professor
Indian Institute of Technology, Chennai.

Dr. G.NAGARAJAN,
Professor & Head,
Pondicherry Engineering College, Pondicherry.

Dr. S. Muttan,
Professor & Head, Department of ECE,
Anna University, Chennai.

Dr. P.H.Rao
Senior Scientist, SAMEER-CEM, Chennai.

Dr. G. Muthuraman
Former Scientist 'G', ISRO.

Mr. S.R.BalaSubramaian,
Independent Director,
Quantum Asset Management Company Pvt. Ltd.

Mr. Satish Mohanram
Sr. Marketing Manager – IndRAA
National Instruments, Bangalore.

Mr. V.Suresh Kumar
Head IT,
Prasad Corp, Chennai.

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Correspondent, CIT
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Emeritus Professor, CIT
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Principal, CIT
Chairman **Dr. A. Rajeswari**
Professor & Head,
ECE Department, CIT.
Organizing Secretaries **Ms.T.Loganayaki,**
Ms.M.Abitha Thangam,
Assistant Professors,
ECE Department, CIT.

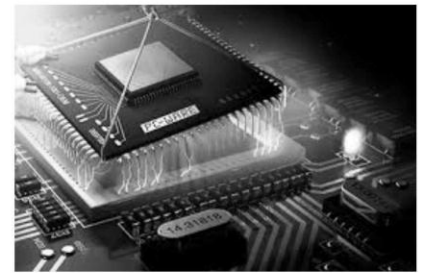
Members

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Dr.S.Uma , Associate Prof., ECE
Prof.T.SenthilVinayakam, Associate Prof.ECE
Prof.P.Chitra, Associate Prof. ECE
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Mr.V.Muralidharan, Asst. Prof. ECE



NATIONAL CONFERENCE ON CIRCUITS, NETWORKS & SYSTEMS (NCCNS'16)

22nd April, 2016



Organized by

DEPARTMENT OF ELECTRONICS AND
COMMUNICATION ENGINEERING
Jointly with
IETE STUDENTS' FORUM
COIMBATORE INSTITUTE OF TECHNOLOGY,
COIMBATORE - 641014 TAMILNADU.

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About Host Institution

Coimbatore Institute of Technology is a Government Aided Autonomous Institution started in the year 1956, by late Shri. V. Rangaswamy Naidu. The Institution has grown from strength to strength and has become a center for academic and Research excellence in various disciplines of Engineering and Technology. The Institute is affiliated to Anna University, Chennai and situated in a campus area of about 25 hectares on the Chennai Cochin (NH 47) highway. C.I.T. is about 7 Km from Coimbatore Railway Junction and 3 Km from Coimbatore Airport.

About the Department

The Department of Electronics and Communication Engineering was established in the year 1968. The department runs an UG course in ECE and PG courses in Communication Engineering, VLSI design and Full time and Part time Ph.D. Research Programmes. The faculty members are highly qualified, well – experienced in teaching and research. The department is equipped with excellent infrastructure to explore the technological challenges in the area of VLSI, Signal Processing, Embedded systems and Communication Systems.

About the Conference

The National Conference on Circuits, Networks and Systems (NCCNS'16) is organized to lay a platform for Electronics & Communication Engineers to design sophisticated Electronic products that handle signals using electronic networks and circuits, which constitutes the complete system. Rapid developments in the analog and digital processing of signals for communication, control, and computer systems have made the theory of electronic circuits, Networks and systems a burgeoning area of research and design. The aim of the NCCNS'16 is to help meet the needs of outlets for significant research papers and state-of-the-art review articles in the area of Electronics and Communication Engineering.

The scope of the conference is broad, ranging from mathematical foundations to practical engineering design. It encompasses, but is not limited to, such topics as linear and nonlinear networks, distributed circuits and systems, multi-dimensional signals and systems, analog filters and signal processing, digital filters and signal processing, statistical signal processing, multimedia, computer aided design, graph theory, neural systems, communication circuits and systems, and VLSI signal processing.

SUBMISSION OF TECHNICAL PAPERS

Technical papers are invited in the areas specified under the title conference themes. Authors may follow IEEE double column format for manuscript preparation. Total no. of papers should not exceed six. Prospective participants are invited to submit papers of their original contribution by email to the Organizing Secretary. The paper must be original and should not have been accepted/ published in Journals or Proceedings. A panel of experts will review the submitted papers. Author(s) shall be notified of the decision as soon as the review process is completed.

Selected papers will be published in Australian Journals of Basic and Applied Sciences.

IMPORTANT DATES

Submission of full paper : March 28th, 2016

Intimation of acceptance : April 13th, 2016

Camera Ready Submission: April 15th, 2016

The final paper in hard copy and soft copy along with the duly filled registration form and fee should reach the organizing secretary by 15th April, 2016 to be included in the proceedings. Detailed instructions will be issued to the authors after the acceptance of the paper. Accepted papers without registration fee will not be included in the conference proceedings.

REGISTRATION FEE

Rs.2,500/- For Delegates From Industries
Rs.2,000/- For Delegates From Academic
Institutions and R & D Organizations
Rs.1,000/- For Research Scholars
Rs.750/- For PG/UG Students

The Registration Fee to be paid by Demand Draft in favour of
"CIT IETE STUDENTS FORUM, CIT, COIMBATORE - 641014"
payable at Coimbatore.

NATIONAL CONFERENCE ON CIRCUITS, NETWORKS & SYSTEMS (NCCNS'16)

22nd April, 2016
REGISTRATION FORM

Name:

Designation: UG / PG / SCHOLAR

Department:

Institution:

Address:

Phone:

Email:

Accommodation required: YES/NO

PAYMENT DETAILS

DD No. :
Dated :
Amount Rs :
Bank Name and Branch :

Place :
Date :

Signature

(Note: This form may be photocopied for additional requirement)