

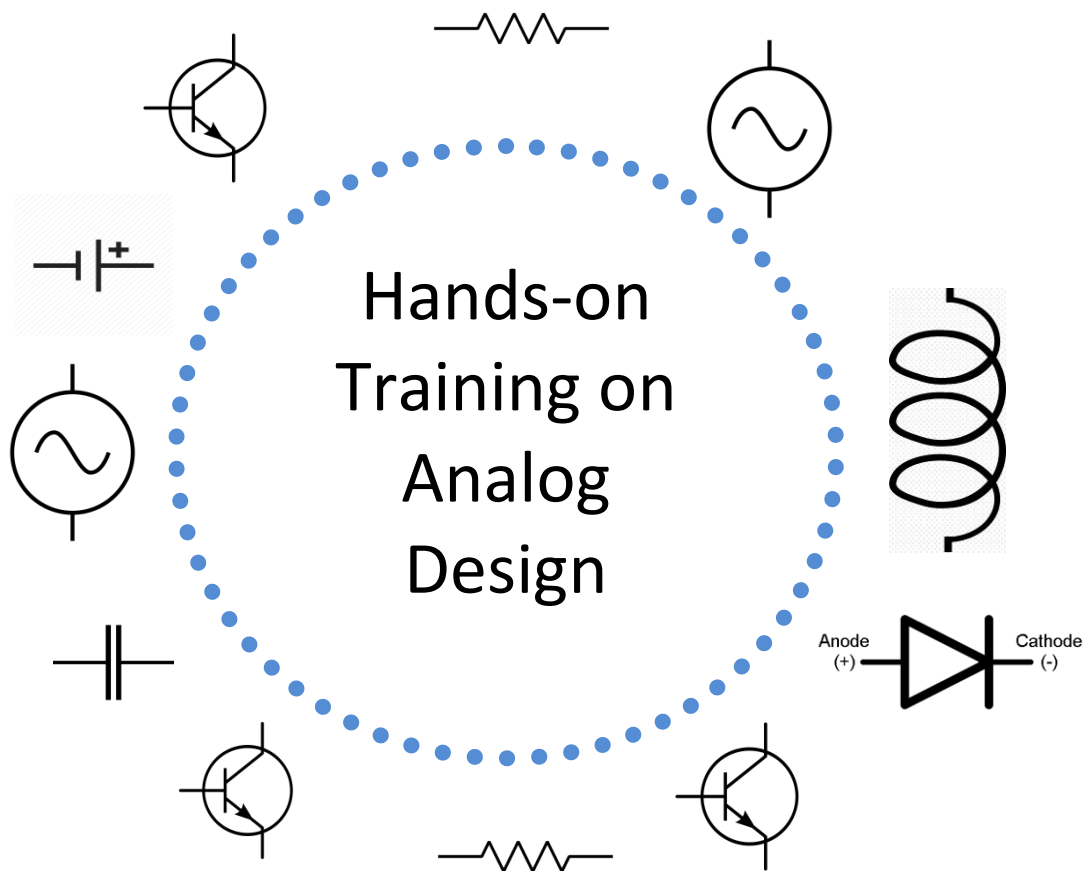


Teaching Learning Centre

(Under Pandit Madan Mohan Malaviya National Mission on Teachers & Teaching Scheme, Dept. of Higher Education, MHRD, GoI)



September 14 & 15, 2018



A Two Day Hands – on Training



Coimbatore Institute of Technology

(Government Aided Autonomous Institution)

(Approved by AICTE, New Delhi & Affiliated to Anna University)

Coimbatore Institute of Technology

Coimbatore Institute of Technology (CIT), Coimbatore, is a Government Aided Autonomous Institution established in the year 1956. The Institute offers Under Graduate, Post Graduate and Ph.D research programmes in Engineering. The Institute is approved by AICTE, New Delhi, Affiliated to Anna University, Chennai and Accredited by National Board of Accreditation (NBA), New Delhi. The institute has a reputation with service of competent, well qualified faculty and dynamic management to set highest standards in engineering research and development. The institute has collaboration with leading frontier universities and industries in India and abroad for the promotion of innovative research and development. National Institutional Ranking Framework (NIRF), MHRD, Govt. of India., has ranked CIT with 1st place in Tamil Nadu for Graduate Outcome and 61st Place in NIRF 2018.

Teaching Learning Centre

Coimbatore Institute of Technology has been approved as Teaching Learning Centre (TLC) under the scheme of Pandit Madan Mohan Malaviya National Mission on Teachers and Teaching (PMMMNMTT), Department of Higher Education, Ministry of Human Resource Development (MHRD) in December, 2017. MHRD has sanctioned grant to establish Teaching Learning Centre in Coimbatore Institute of Technology for Raising the Quality of Teaching through training, retraining, refresher and orientation programmes in the disciplines of Academic, Research and Management (ARM). CIT – TLC is established to improve the quality of teachers of technical education institutions in the regions of Tamil Nadu (West), Kerala, Karnataka, Andhra Pradesh and Puducherry.

About the Workshop

This two days Hands on Training is aimed in understanding the importance of analog design with the flavor of blended learning. Build a circuit diagram and understand the mathematical model. Simulate the circuit diagram and verify the mathematical model. See to believe the same on a hardware platform, outputs tally with the simulation result, and as per the mathematical model.

Hands on Training Modules

Simulation session

- Introduction to the TI Analog simulator tool
- Build the circuit (schematics capture) diagram & check ERC
- Select the type of excitation for the circuit
- Understand the mathematical model
- Verify the output waveform as per mathematical model

Hardware verification session

- Understanding of the Hardware platform
- Download the code to the hardware platform to generate the simple excitation signal
- Alternatively use signal generator to generate the input excitation signal
- Verify the output as per the simulation result, using oscilloscope

Prof. Ashok Rao

Prof. Ashok Rao holds a BE, ME & PhD all in EE from Mysore University (1982), Indian Institute of Science, Bangalore (1985) and Indian Institute of Technology, Bombay (1991) respectively. He specializes in Digital Signal Processing area and is known to be one of the leading figures in this area in India. In addition, he teaches Image Processing, Multimedia, Linear Algebra, Renewable Energy, BioMetrics, and ICT. He has over 25 years of Teaching & Research Experience in IIT Bombay, IISc Bangalore and in the rest in various places in India. He has successfully guided Ph.D.'s both in E & C and Computer Science areas and has over 80 publications.

He is also a Solar Photo Voltaic (SPV) specialist having been trained by Siemens Solar California during 1996-1997. **Earlier he was Head, Network Project at CEDT, IISc Bangalore from 1999-2005.** It was a project funded by Swiss Government and covered E & C Engineering education in the whole of South India, this is among the very few successful higher education projects in India.

From 1999, he has been a Training consultant on Design of DSP & Multimedia systems to Philips, NXP, Techtronic's, Honeywell, Toshiba, SONY, Infineon, Texas Instruments, Analog Devices, etc. He has been awarded Texas Instruments (TI) International DSP Design & Education Award for Promoting Excellence in Under Graduate DSP education in India during 1996-98. He is also an Entrepreneur having successfully started several ICT & Renewable Energy companies during 1998 to 2009. He is one of the very few to be invited to make a presentation in a PAN-IIT-2006 global meet. He has been a Guest professor at IIM Bangalore from 2007 teaching PGPP and PGSEM programs and executive MBA programs there.

Prof. S.M. Narasimhan

Prof. S.M. Narasimhan has 30+ years of experience in Industry, Research, teaching, enterprising activities and project management. His experience includes research in commercial, industrial as well as military projects, design, development, manufacturing, testing conformal verification and field support. Some of the industries he has worked with are ITI, Dooravani Nagar Bangalore, ST Electronics Singapore, Saragur systems Singapore, Orthodyne Electronics, USA. His research interests are in the areas of embedded systems, IOT, DSP Architectures, FPGA design, coding etc, and motivating young minds towards enterprising activities.

Registration Details

Eligibility

Faculty Members and Full Time Research Scholars of **Electrical / Electronics / Instrumentation Engineering** disciplines are eligible to apply for this programme

Registration Procedure

- No Registration / Participation fee will be collected
- Interested participants are requested to register through the following Google Form link on or before September 11, 2018.

https://docs.google.com/forms/d/e/1FAIpQLSd7VAZCTwfG1vub352T_mUIXO-mwhfcxwaLvpYJlQzEuf5Y_A/viewform?usp=sf_link

- Number of participants is limited to 60
- Selected participants will be intimated through e-mail by September 12, 2018
- Accommodation will be provided on prior request
- For queries please contact **tlcp4@cit.edu.in**
- Participants should upload permission letter (attached in this brochure) duly signed by the Head of the Institute / Director

Organizing Committee

Chief Patron

Dr.S.R.K.Prasad

Correspondent, CIT Institutions, Coimbatore.

Patrons

Mr.S.Rajiv Rangasami

Director, CIT Institutions, Coimbatore.

Dr.R.Prabhakar

Secretary, CIT Institutions, Coimbatore.

Chairperson, CIT – TLC, Coimbatore.

President

Dr.V.Selladurai

Principal, CIT, Coimbatore

Co-Chairperson, CIT – TLC, Coimbatore.

Convener

Dr.V.Manikandan

Project Coordinator, CIT – TLC,

Coimbatore.

Coordinator(s)

Dr.M.Vijay - Project Officer, CIT – TLC, Coimbatore.

Dr.K.Shri Akiladevi – Research Officer, CIT – TLC, Coimbatore.

Venue

Coimbatore Institute of Technology

Avinashi Road, Civil Aerodrome Post, Peelamedu,

Tamil Nadu, India – 641 014

Contact Details

Dr.M.Vijay

Project Officer,

Teaching Learning Centre,

Coimbatore Institute of Technology,

Coimbatore.

tlcp4@cit.edu.in

Ph: 8903136122

Permission Letter to Attend Two Day Hands-on
Training on
Analog Design
September 14 & 15, 2018

**** This Permission Letter should be uploaded in Google Registration Form**

Mr/Ms/Dr _____ of the Department
_____ has permitted to attend Two Day Hands - on
Training on Analog Design at Teaching Learning Centre of Coimbatore Institute of Technology, Coimbatore from
14 to 15 September, 2018.

Name of the College :

Place :

Date :

Signature of Head of the Department

Signature of Head of the Institution / Director