

ORGANIZING COMMITTEE

Chief Patron
Dr. S.R.K. PRASAD,
Correspondent
CIT Institutions
Coimbatore -14.

Patron
Dr. R. PRABHAKAR,
Professor Emeritus and Secretary
CIT Institutions
Coimbatore -14.

President
Dr. V. SELLADURAI,
Principal
CIT Institutions
Coimbatore -14.

Convener
Dr. S. VASANTHARATHNA,
Professor and Head
Department of EEE
Coimbatore Institute of Technology
Coimbatore -14.

Coordinators
Dr. S.SUJA
Associate Professor
Department of EEE
Coimbatore Institute of Technology
Coimbatore -14.

Mr S.GUNASEKARAN.
Assistant Professor
Department of EEE
Coimbatore Institute of Technology
Coimbatore -14.

Mrs. M.L. MERLIN SAJINI,
Assistant Professor,
Department of EEE,
Coimbatore Institute of Technology,
Coimbatore -14

TARGET AUDIENCE

This one week short term course is deliberately designed for the professionals working in:

- Faculty from Academic Institutions / FullTime Research Scholars
- Research and Practicing Engineers
- Public/Private Industries & R&D Establishments

REGISTRATION DETAILS

Selection process is based on first come first serve basis. The registration fee of RS.250/- should be drawn through DD in favor of "QIP-ATSMD" payable at Coimbatore to the organizing committee. The DD number and date should be mentioned in the registration form. After selection, the DD should be sent with the candidates name and official address at the backside of DD. The interested participants are requested to send the filled in registration form duly signed by the Head of the Institution to the address mentioned below on or before the last date.

TRAVEL ALLOWANCE

The selected outstation participants from academic institutions are eligible for 3-Tier AC train fare by the shortest route with proper proof.

BOARDING AND LODGING

Free Boarding and lodging facilities will be provided for the participants from AICTE approved govt/govt aided institutions on twin sharing basis in the College Hostel/Guest house. No family accommodation will be provided.

RESOURCE PERSONS

Faculty experts are from premier academic institutions and invited experts from leading industries, research and development organizations.

IMPORTANT DATES

Last Date for Registration	15 APRIL 2016
Selection Confirmation	20 APRIL 2016

Address for Communication :

Dr. S.SUJA,
Associate Professor,
Department of Electrical and Electronics Engineering,
COIMBATORE INSTITUTE OF TECHNOLOGY,
Avinashi Road, COIMBATORE 641 014.
TAMIL NADU, INDIA.
Mobile : 9442064263, 9789751423, 7708614550
E-mail : qipatsmdcit@gmail.com



AICTE SPONSORED

Quality Improvement Programme

One Week Short Term Course
on

ADVANCED TECHNOLOGY IN
SMART GRID

2nd – 8th MAY, 2016



Organized by

Department of
Electrical and Electronics Engineering

COIMBATORE INSTITUTE OF
TECHNOLOGY

(A Govt. Aided Autonomous Institution
Affiliated to Anna University, Chennai)

Coimbatore – 641014,
Tamil Nadu, India.

Phone: (0422) 2574071, 2574072
Fax: (0422) 2575020

Website: www.cit.edu.in

HOST INSTITUTION

Coimbatore Institute of Technology (CIT) started in 1956 by Sri. V. Rangasamy Naidu is reckoned for its academic excellence in engineering and technology. CIT is a Government Aided Autonomous Institution affiliated to Anna University, Chennai. The institute has a reputation with service of competent, well qualified faculty and dynamic management to set highest standards in engineering research and development. CIT offers under graduate, post graduate and PhD programs with a global standard curriculum promoting the students to compete internationally. The institute is celebrating its Diamond Jubilee in the year 2016. The institute has collaborated with leading frontier universities and industries in India and abroad for the promotion of innovative research and development. CIT has been awarded with the "Biz-2012" the world business leader award and also honored with the Outstanding Engineering Institute (SOUTH) for consecutive seventh time awarded by ABP NEWS National Education Awards.

HOST DEPARTMENT

The Department of Electrical and Electronics Engineering is one of the earliest departments in CIT which offers both under graduate (EEE), post graduate (Embedded and Real Time Systems and Applied Electronics) and Ph.D. programmes. The department has a team of well experienced faculty members having vast experiences in various engineering and technology with specialization in the areas like Power System, Power Electronics, Power Quality, Energy Engineering, Electrical Machines, Control Systems, Embedded Systems, VLSI, Robotics and Wireless communications. Solar PV panel, wind turbine integrated with Grid has been installed and commissioned in the department. The main objective of the department is to create a highly skilled industry ready manpower to deal with the technical challenges faced by the scientific and technical industries.

PREFACE ABOUT THE SHORT TERM COURSE

Smart grid (SG) is an adaptable and responsive electricity grid that supports changes and the needs of consumers now and in the future. SG provides solution to lower losses in transmission and distribution. SG technologies can anticipate and monitor demand minimizing the overcapacity and making the grid more reliable. SG takes care of outages also. The power outages drag down the economy. SG provides technologies to anticipate and prevent problems before they happen. Whenever unplanned outages occur, smart grid outage management systems can reroute power to minimize the outage, analyze needed repairs and dispatch crews more effectively to make the grid more efficient. More importantly, it enables the industries and academicians best ideas for grid modernization to achieve their full potential. The two-way digital communication and plug-and-play capabilities exemplify a smarter grid. It provides consumers with the ability to use electricity more efficiently and provides utilities with the ability to detect problems on their systems and operate them more efficiently.

The FDP aims at providing a forum for members of the research and academic community to interact on cutting-edge technologies.

COURSE CONTENTS

- (1) Distribution and Automation in SG
- (2) Power Electronics in SG
- (3) Signal Processing in SG
- (4) Soft Computing Techniques in SG
- (5) Nonlinear Dynamics and Controlling in SG
- (6) Wireless Communication in SG
- (7) Sensor Networks in SG
- (8) Spot Pricing

ELIGIBILITY AND SELECTION

This Programme is open to the faculty members of various departments of AICTE approved Engineering Colleges, and from industry/ research organizations. The total intake is 30 and selection will be based on first come first serve basis.

REGISTRATION FORM

AICTE SPONSORED
Quality Improvement Programme
Short Term Course on

ADVANCED TECHNOLOGY IN SMART GRID
2nd – 8th MAY, 2016

Name Dr/Mr/Ms
Designation.....
Department.....
Institution/Organization.....
Official Address.....
.....
E-mail..... Fax No.
Tel. No. (O)
Mobile.....
Hostel accommodation required Yes/No
Demand Draft No..... dated.....
Amount in Rs 250 Drawn at

Place :
Date :
Signature of the Applicant

SPONSORING CERTIFICATE

This is to certify that Dr./Mr./Ms
of department of
college is being sponsored for attending the AICTE-QIP Sponsored one week "STC on "....." organized by Department of EEE, Coimbatore Institute of Technology during 2nd to 8th of May 2016 if selected. The college is approved by AICTE.

Signature of the Head of the Institution
(With seal)