

TEQIP-II Sponsored One week Faculty Development Programme on
DIGITAL AND ANALOG VLSI DESIGN
20.11.2013 to 26.11.2013
Department of Electronics and Communication Engineering
Coimbatore Institute of Technology, Coimbatore

The Department of Electronics and Communication Engineering, Coimbatore Institute of Technology organized TEQIP-II Sponsored One week Faculty Development Programme on DIGITAL AND ANALOG VLSI DESIGN during 20.11.2013 to 26.11.2013. The one week FDP on DIGITAL AND ANALOG VLSI design has been intended for the working electronic design engineers and faculty members to give an overview of VLSI Design and Technology, recent trends in VLSI and in-depth training in the use of Electronic Design Automation tools for designing digital and analog systems. This course will be of great experience for the people who are interested in doing research in VLSI Design and setting up VLSI lab and Analog IC Design as well.



The sessions were handled by technical experts from renowned Institutions and Industry Experts. The programme started with an Inaugural function on 20-11-2013. Mr. Anbumani Subramani, Intel, Bangalore, chief guest of the function, gave the Inaugural address and a lecture on “VLSI Design Challenges and applications in Computer Vision”. Followed by this session Mr. Vijay Kumar PS, Application Engineer from Cadence, Bangalore gave a talk on Introduction to VLSI Design and Custom IC Design Flow using Cadence Tool. Mr. Ramesh. M, Application Engineer from VI Micro Systems, Coimbatore addressed the participants on ASIC Design Flow using Cadence Tool.

Day two of the programme included Circuit Characterization, Combinational Circuit Design & FPGAs by Dr. P.Kalpana from PSG College of Technology, Coimbatore. Afternoon had a lecture on CMOS Testing, by Dr.C.Vasanthanayaki and Low Power VLSI by Dr. Deepa from Government College of Technology, Coimbatore.

Third Day lectures were given by eminent personalities from NIT, Trichy. Dr.G. Lakshminarayanan discussed on Sequential Circuit Design and Pipelining & Parallel Processing. Dr. M. Bhaskar had an interactive session on Interconnects in FPGAs.

Fourth Day, an Industrial visit was arranged to M/s . Vasantha Advanced Systems, Coimbatore and Isha Yoga Centre.

Sunday morning session was on Engineering Ethics handled by Dr. S. Uma Maheswari, CIT. Followed by this session was an Introduction to Digital Circuit Design in VHDL using Altium Designer Tool by Dr. S. Uma , CIT. Afternoon session was a demonstration on Digital Circuit Design in VHDL using Altium Designer Tool by Ms. G. Puvaneswari and Ms. I.S Akila, CIT.



On Sixth day, Mr. Shrikanth Atkarne, Senior Application Engineer, M/s.nilogic Pvt. Ltd, Pune discussed on Full custom and Semi custom IC Design and CMOS Circuit Design using Micro Wind Tool. Mr. Tamarai Kannan, Senior Application Engineer, M/s.Virtual Technologies, Coimbatore had interesting sessions on Digital Design & Analysis using Micro Wind Tool.

Last day Mr. Tamarai Kannan continued with Analog Design & Analysis and Field Programmable Analog Array. Finally, the FDP programme ended with a Feedback Session and Valedictory Function.