

**COIMBATORE INSTITUTE OF TECHNOLOGY, COIMBATORE - 14
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING**

**TWO DAY FACULTY DEVELOPMENT PROGRAM ON VLSI DESIGN
USING TANNER EDA & MENTOR GRAPHICS TOOLS
13th and 14th August 2013**

REPORT

The Department of Electronics and Communication Engineering, Coimbatore Institute of Technology organized a Two Day Faculty Development Programme jointly with Trident Techlabs Pvt. Ltd., Chennai on VLSI DESIGN using TANNER EDA and MENTOR GRAPHICS TOOLS on 13th and 14th August 2013. The two day FDP on VLSI design has been intended for the working electronic design engineers and faculty members to give in-depth training in the use of Electronic Design Automation tools for designing digital and analog systems. Emphasis was laid on Tanner EDA and Mentor Graphics tools.



The sessions were handled by technical experts from Trident Techlabs Pvt. Ltd., Chennai. The sessions of day one on 13th August 2013 were dedicated to VLSI FPGA design flow by Mr.V.Baranidharan, Technical Manager, Techlabs with a demonstration, which was followed by hands on training. The sessions on day two on 14th August 2013 started with a presentation on VLSI ASIC design flow by Mr.G.Vijay, Field Application Engineer, Techlabs followed by a demonstration and hands on training on Tanner EDA in the afternoon. Both days, the lab sessions were handled by Mr.S.Anand, Senior Field Application Engineer and Mr.V.Baranidharan, Technical Manager, Techlabs.

